

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Christos John Georgiou, et. al.

Group Art Unit: Unknown

Serial No.: 10/604295

Filed: 7-9-2003

Examiner: unknown

For: METHOD AND SYSTEM OF DATA TRANSFER FOR EFFICIENT MEMORY UTILIZATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT **UNDER 37 C.F.R. 1.56**

Sir:

Under provisions of 37 C.F.R. 1.97 through 1.99 and pursuant to applicant's duty of disclosure under 37 C.F.R. 1.56, applicants respectfully bring the documents listed on the attached Form PTO-1449 to the attention of the Examiner in charge of the above-identified application. A copy of each listed document is provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the cited references are relevant or material to the claims nor should it be construed as a representation that no other art than that identified exists. They are merely cited as constituting related art of which the applicant is aware.

DOCKET NO. BUR920030037US1

It is respectfully requested that these documents be considered by the Examiner and formally made of record in this application.

Respectfully submitted,

Andrew M. Calderon

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FORM PTO-1449 (Modified)			ATTY. DOCKET NO. BUR920030037US1	S	SERIAL NO. 10/295			
	ENTS AND PUBLICATION DIS		APPLICANT: Christos John Georgiou et al.					
(Use several sheets if necessary)			FILING DATE:			OUP: assigned		
REFERENCE D	ESIGNATION	U.S. PA	ATENT DOCUMENTS					
EXAMINE R INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	1	G DATE PPRO.)	
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	1	FOREIGN	N PATENT DOCUMENTS					
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLAS S	TRANSLATIO N YES NO		
	WO9419748A2	9/01/1994				165	NO	
	ОТНЕ	R ART (Including	Author, Title, Date, Pertino	ent Pages, etc.)	1		
	PARADISE: An A	Advanced Featured	Parallel File System by Mac	iej Brodowicz	and Olin Johns	on		
			Article 16 Page 654, "Fast, S om Slow Memory Elements"		With Multiple	Output	Queues	
	Object Resizing ar	d Reclamation thro	ough the use of hardware, by	Witawas Srisa				
	IBM TDB n3 08-9	2 pgs 382-385 "Da	ta Transfer with Virtual Men	nory Queus"	· · · · · · · · · · · · · · · · · · ·			
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